

①  
+10

④  $I_B = \frac{5 - 0.7}{4.3k} = \boxed{1 \text{ mA}}$

$$I_c = \frac{12 - (V_{CE,sat} + V_F)}{620\Omega}$$

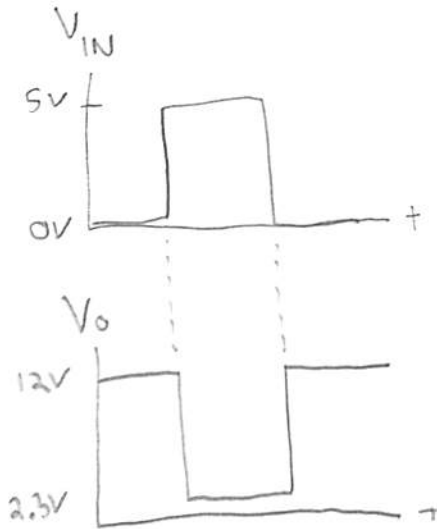
$$= \frac{12 - (0.3 + 2)}{620} = 0.0156 \text{ A} = \boxed{15.6 \text{ mA}}$$

$$V_o = 12 - (0.0156 \text{ A})(620) = \boxed{2.3 \text{ V}}$$

⑥  $\frac{I_c}{I_B} = \frac{15.6 \text{ mA}}{1 \text{ mA}} = 15.6 \approx \frac{0.1 \times 150}{15}$

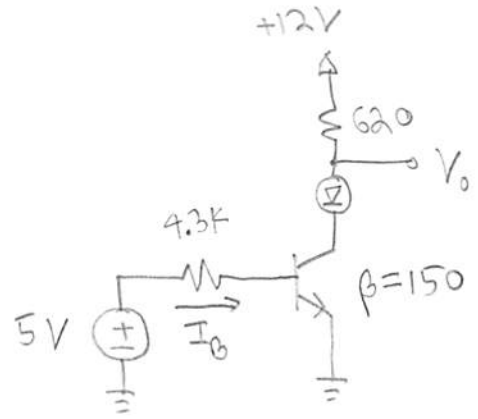
**YES, hard saturation**

⑦



close enough

Transistor is ON



2 (10)

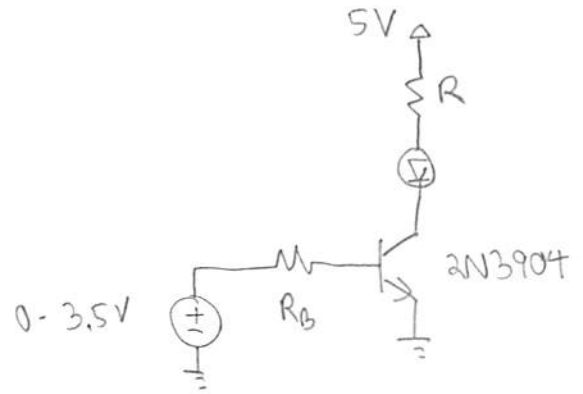
(a) First choose R:

$$5 - (.010)R - 2.5 - V_{CE,sat} = 0$$

0.2V

$$R = \frac{5 - 2.5 - 0.2}{.010} = 230\Omega$$

Choose  $R = 220\Omega$



Determine  $R_B$  by hard saturation:

$$I_B = \frac{3.5 - V_{BE,sat}}{R_B} \sim \frac{.010A}{0.1 \beta_{min}}$$

0.85V

100

$$R_B \sim (3.5 - 0.85) \frac{0.1 \times 100}{.010A} = 2650\Omega$$

Choose  $R = 2.7K$   
(2.4K is also OK)

(b)  $P = I^2 R = (.010)^2 (220\Omega) = .022W = 22mW$

(c)  $2 \times 22mW = 44mW \leftarrow \frac{1}{8}W \text{ resistor is fine}$

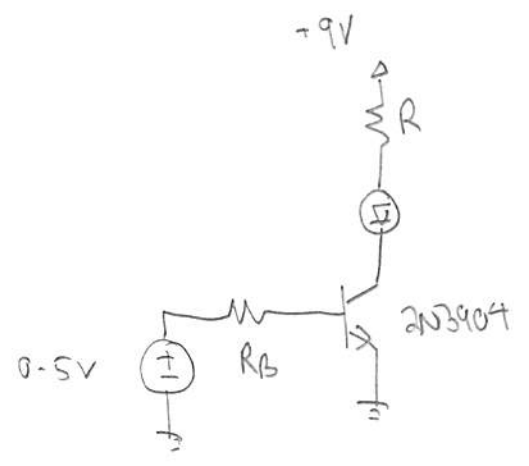
③ (a) First determine R:

$$9 - (.030)R - 3.5 - V_{CE,sat} = 0$$

↑  
0.3V

$$R = \frac{9 - 3.5 - .3}{.030} = 173 \Omega$$

Choose  $R = 160 \Omega$



Determine RB:

$$I_B = \frac{5 - V_{BE,sat}}{R_B} \sim \frac{.030}{0.1 \beta_{min}}$$

↑ 0.9V

↑ 80

$$R_B \sim (5 - 0.9) \frac{0.1 \times 80}{.030} = 1093 \Omega$$

Choose  $R_B = 1k$

(1.1k is fine)

⑥  $P = I^2 R = (.030)^2 (160) = .144 W$

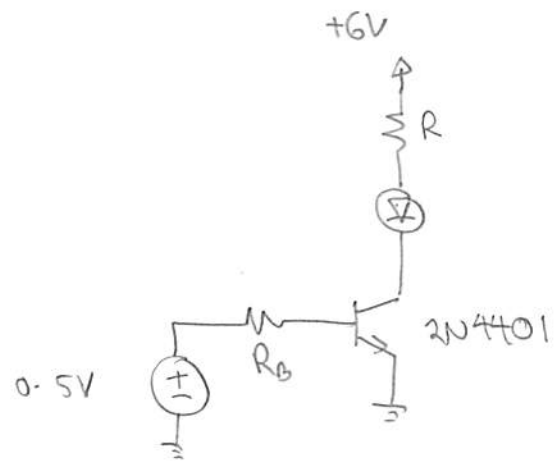
$2 \times .144 = .288 W$  choose  $\frac{1}{2} W$  resistor

④ +10

a) Determine R:

$$6 - (.3)R - 3.5 - V_{CE,sat} = 0$$

~0.6V



$$R = \frac{6 - 3.5 - 0.6}{.3} = 6.33\Omega$$

Choose  $R = 6.2\Omega$

Determine  $R_B$ :

~1.1V

$$I_B = \frac{5 - V_{BE,sat}}{R_B} \sim \frac{.300A}{0.1\beta_{min}} = .043A$$

~70

$$R_B \sim (5 - 1.1) \times \frac{1}{.043A} = 91\Omega \quad \text{Choose } R_B = 91\Omega$$

b)  $P = I^2 R = (.3)^2 (6.2) = 0.558W$

$2P = 1.116W$  Choose  $2W$  resistor (safer)

1W is probably OK

Can also add  $(.043A)(1.1V) = 47mW$  for base current

c)  $P_{MAX} = 625mW - (5mW/^{\circ}C)(60 - 25) = 450mW$

d)  $P = I_E V_{CE} = (.3A)(6 - .3 \cdot 6.2 - 3.5) = 0.192W = 192mW$

e)  $2P = 384mW < 450mW$

No heat sink needed

If we include base current contribution,

$2P = 478mW$

Heat sink may be needed ...

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$$\textcircled{a} \quad V_+ = V_a \frac{R_2}{R_1 + R_2}$$

$$I = \frac{V_b - V_-}{R_3} = \frac{V_- - V_{out}}{R_4}$$

$$V_+ \frac{R_4}{R_3} V_b - \frac{R_4}{R_3} V_- = V_- - V_{out}$$

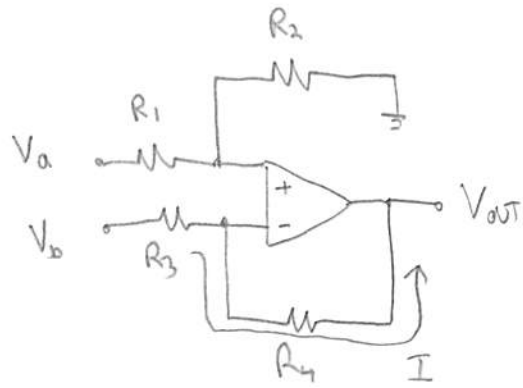
$$V_{out} = V_- \left(1 + \frac{R_4}{R_3}\right) - \frac{R_4}{R_3} V_b$$

Since  $V_- = V_+$ :

$$V_{out} = V_a \frac{R_2}{R_1 + R_2} \left(1 + \frac{R_4}{R_3}\right) - \frac{R_4}{R_3} V_b$$

$$\textcircled{b} \quad R_3 = R_1, \text{ and } R_4 = R_2: \quad V_{out} = V_a \frac{R_2}{R_1 + R_2} \left(1 + \frac{R_2}{R_1}\right) - \frac{R_2}{R_1} V_b$$

$$= V_a \frac{R_2}{R_1} - \frac{R_2}{R_1} V_b = \frac{R_2}{R_1} (V_a - V_b)$$



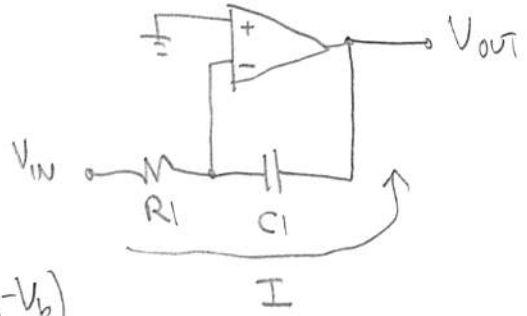
6 (10)

a)  $V_+ = 0$   
 $V_- = 0$

$$I = \frac{V_{IN} - 0}{R_1}$$

For capacitor,

$$i = C \frac{d}{dt} (V_a - V_b)$$



$$\text{So, } I = \frac{V_{IN}}{R_1} = C_1 \frac{d}{dt} (0 - V_{OUT})$$

$$\frac{dV_{OUT}}{dt} = -\frac{1}{R_1 C_1} V_{IN}$$

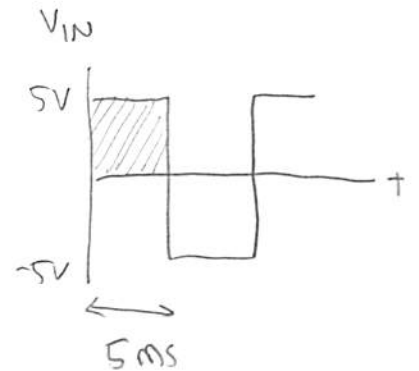
$$V_{OUT} = -\frac{1}{R_1 C_1} \int V_{IN} dt$$

b)

$$R_1 C_1 = (10^5 \Omega)(10^{-7} F) = .01$$

$$V_{OUT} = -\frac{1}{.01} \int V_{IN} dt = -100 \int V_{IN} dt$$

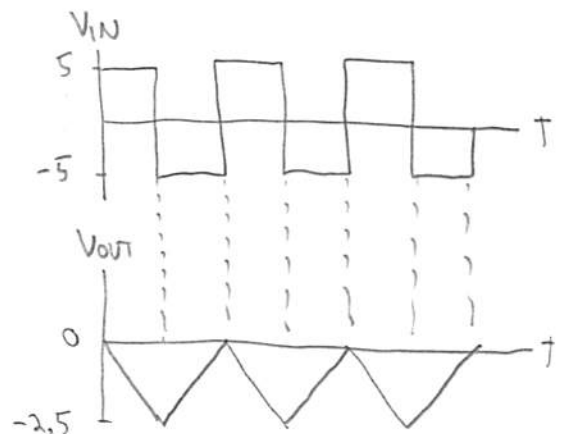
$$100 \text{ Hz} \Rightarrow T = .01 \text{ s} = 10 \text{ ms}$$



For  $0 < t < 5 \text{ ms}$ ,  $\int V_{IN} dt$  is a ramp

$$\int_0^{.005} 5 dt = 5 \times .005 = .025$$

$$\begin{aligned} \Rightarrow V_{OUT} &= -100 \times .025 \\ &= \underline{\underline{-2.5V}} \end{aligned}$$



7 (10)

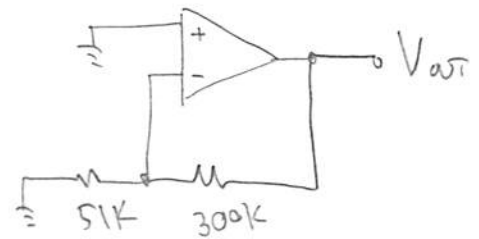
a)  $R_{in} = 51K$

b) For 741C op amp:

$$I_{IN(BIAS)} = 500 nA$$

$$I_{IN(OFFSET)} = 200 nA$$

$$V_{IN(OFFSET)} = 6 mV$$



$$G = -\frac{300K}{51K} = -5.88$$

$$R_{TH(+)} = 0$$

$$R_{TH(-)} = 51K // 300K = \underline{43.6K}$$

Input bias current:  $\Delta V_{IN} = (500 nA)(43.6K - 0) = 0.0218V$

$$\Delta V_{out} = 5.88 (0.0218) = \boxed{0.128V}$$

Input offset current:  $\Delta V_{IN} = (200 nA) \left( \frac{43.6K + 0}{2} \right) = 0.0044V$

$$\Delta V_{out} = 5.88 (0.0044) = \boxed{0.026V}$$

Input offset voltage:  $\Delta V_{out} = 5.88 (0.006) = \boxed{0.035V}$

c) Main source of error is input bias current

d) Worst case  $\Delta V_{out} = 0.128 + 0.026 + 0.035 = \boxed{0.189V}$

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a)  $R_{IN} = 51K$

b)  $I_{IN(BIAS)}: \Delta V_{OUT} = 5.88 \times (400pA)(43.6K - 0) = 103 \mu V$

$I_{IN(OS)}: \Delta V_{OUT} = 5.88 \times (200pA) \left( \frac{43.6K + 0}{2} \right) = 26 \mu V$

$V_{IN(OS)}: \Delta V_{OUT} = 5.88 \times 0.015 = 88 mV$

c) Main source of error is input offset voltage

d) Worst case  $\Delta V_{OUT} = 0.103 mV + 0.026 mV + 88 mV = 88.129 mV$

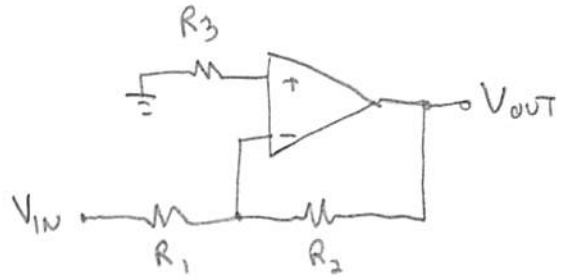


9

$$G = -\frac{R_2}{R_1} = -50$$

Many combinations of  $R_1$  and  $R_2$  are OK.

$$\text{let } \begin{cases} R_1 = 20\text{K} \\ R_2 = 1\text{M} \end{cases}$$



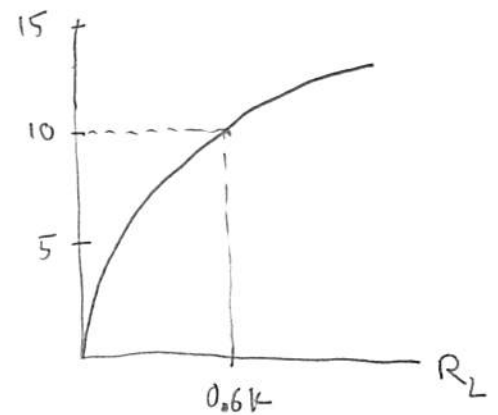
$$\textcircled{b} \quad R_3 = R_1 \parallel R_2 = 20\text{K} \parallel 1\text{M} = 19.6\text{K}$$

$$\text{choose } R_3 = 20\text{K}$$

$$\textcircled{c} \quad V_{\text{out}} = -50 \times (-0.2\text{V}) = \underline{10\text{V}}$$

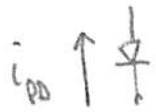
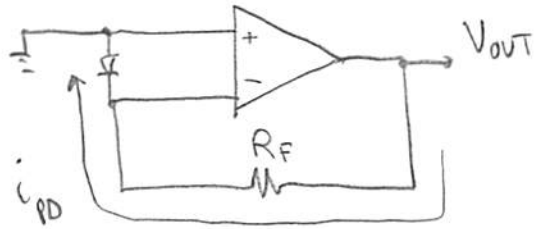
According to plot of output

$$\text{voltage swing, } R_L \geq 0.6\text{K}$$



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- (a) Choose the left circuit because photocurrent always flows from cathode to anode.



Therefore,  $V_{OUT} > 0$  for above circuit.

- (b) According to plot,  $I_{PD} \sim 3.5 \mu A$

$$\text{SO, } (3.5 \mu A) \times R_F \geq 5V$$

$$R_F \geq \frac{5V}{3.5 \mu A} = 1.43M$$

choose  $R_F = 1.5M$

(c)  $R_{TH(+)} = 0$

$$R_{TH(-)} = R_F = 1.5M$$

$$\Delta V_{OUT} = (400 pA)(1.5M - 0) + (200 pA)\left(\frac{1.5M + 0}{2}\right) + 0.015$$

$$= 0.0006 + 0.00015 + 0.015$$

$$= 0.01575 V = \boxed{15.75 mV} \leftarrow \text{Pretty small}$$

↑  
Voltage gain is effectively equal to 1